

1. Function Description

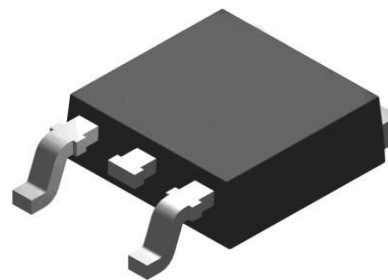
SL4284 is a NPN type monolithic integrated fixed voltage regulator in a 3-pin TO package, with a typical drive current of 400mA, and the chip package TO252-3. The chip is applied to driving of micro-processor systems or automobile applications of several conditions; in addition, it has such as overloading protection, short circuit protection and over-temperature protection.

If the input voltage V_I is within the ranges of $(V_Q + V_{dr}) < V_I < 55V$, it is regulated to V_Q , and the voltage difference V_{dr} changes between 0.3V and 0.5V according to the size of the driving current.

2. Characteristics

- Rated output voltage 5V
- Typical output current 400mA
- Low drop
- Short circuit protection
- Over-temperature protection
- Input voltage as high as 55V
- Working temperature ranges
 $T_{op} = -40 \sim 125^{\circ}C$

- RoHS



TO252-3L

3.Description of pins

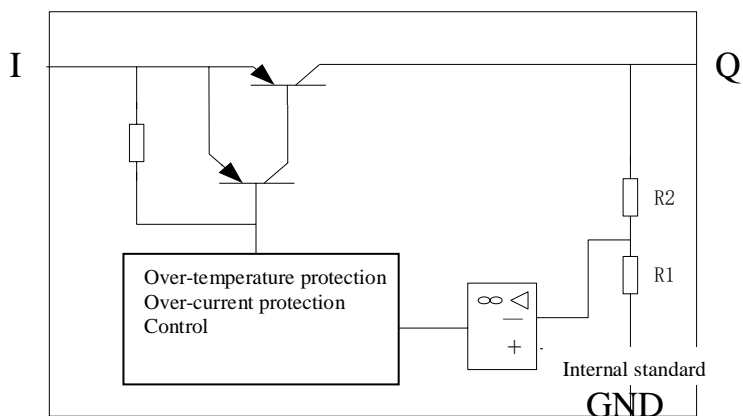


Figure 3- 1 SL4284 Fixed output voltage module block diagram

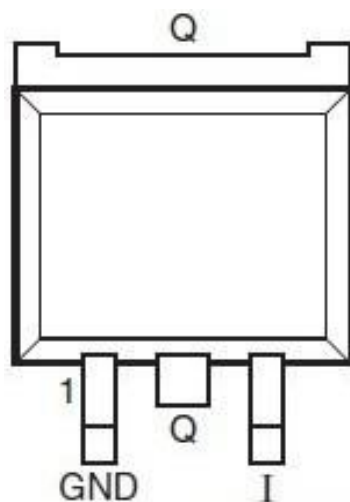


Figure 3-2 Pin configuration (top view)

Table 3.1 Pin definition and functions

No. of pin	Symbols	Function
1	GND	Grounding end
2, Tab	Q	Output; connect to ground using a capacitor with $C_Q \geq 10\mu\text{F}$ and $\text{ESR} \leq 10\Omega$ at 10KHz. connected to the cooling
3	I	Input

4. Electrical parameters

Table 4.1 Scope of work

Parameters	Symbols	Parameter values		Units	Remark
		Min	Max		
Input Voltage	VI	VQ+Vdr	55	V	
Junction temperature	Tj	-40	150	°C	

Table 4.2 Absolute Maximum Ratings

Tj=-40°Cto150°C.All the voltage values are relative to ground unless otherwise specified.

Parameters	Symbols	Limiting value		Units	Remark
		Min	Max		
Input and output voltage difference	VI-VQ	-0.3	50	V	
Input Voltage	VI	-0.3	55	V	
Output voltage	VQ	-0.3	12	V	

ESD withstanding voltage

HBM	Voltage		4	KV	
CDM	Voltage		400	V	
Temperature	Tj	-40	150	°C	Junction temperature
	Tstg	-40	150	°C	Storage temperature

Thermal resistance

Thermal resistance	Rthj-a	50	90	K/W	Only pin
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- 1) The ESD withstanding voltage human body model is designed according to JESD22-A114.
- 2) The ESD withstanding voltage charging/discharging equipment model is designed according to JESD22-C101.

Remarks: The voltage listed above may lead to permanent injury to the chip, and long-term exposure in the maximum rated value may lead to influences on reliability of the device.

Table 4.3 Electrical Characteristics
 $V_I = 13.5V, -40^{\circ}C \leq T_j \leq 150^{\circ}C$, unless otherwise specified.

Parameters	Symbols	Parameter values			Units	Note
		Min	Typ	Max		
Output voltage	VQ	4.85	5.00	5.15	V	$10 \leq I_Q \leq 400mA$; $6.4V \leq V_I \leq 16V$
			5.0		V	$10 \leq I_Q \leq 400mA$; $16V \leq V_I \leq 40V$ ¹⁾
Linear adjustment rate	ΔV_{QLi}			10	mV	$6.4V \leq V_I \leq 40V$
Load regulation ratio	ΔV_{QLo}			100	mV	$10mA \leq I_Q \leq 400mA$ ²⁾ $V_{IN} V_I = V_{Qnom} + V_{dr}$
Load adjustment rate	Vdr		0.3	0.5	V	$I_Q = 300mA$ ³⁾
Quiescent current	Iq		90	120	uA	$I_Q = 10mA$
Output current limiting	$I_{Q,max}$	400	800	1100	mA	$V_I - V_Q < 18V$; $V_Q = V_{nom} - 100mV$
RMS output noise			30		ppm	VQ ppm $T_j = 25^{\circ}C$ $10Hz \leq f \leq 10KHz$
Power supply rejection ratio	PSRR		65		dB	$F_r = 120HZ$ $V_r = 0.5V_{pp}$

1) $T_j < 125^{\circ}C$, avoid chip over temperature

2) The junction temperature keeps constant during testing.

3) Voltage difference = $V_I - V_Q$ (it is tested when 100mV drop when compared with the rated voltage at $V_I = 13.5V$).

5. Application Information

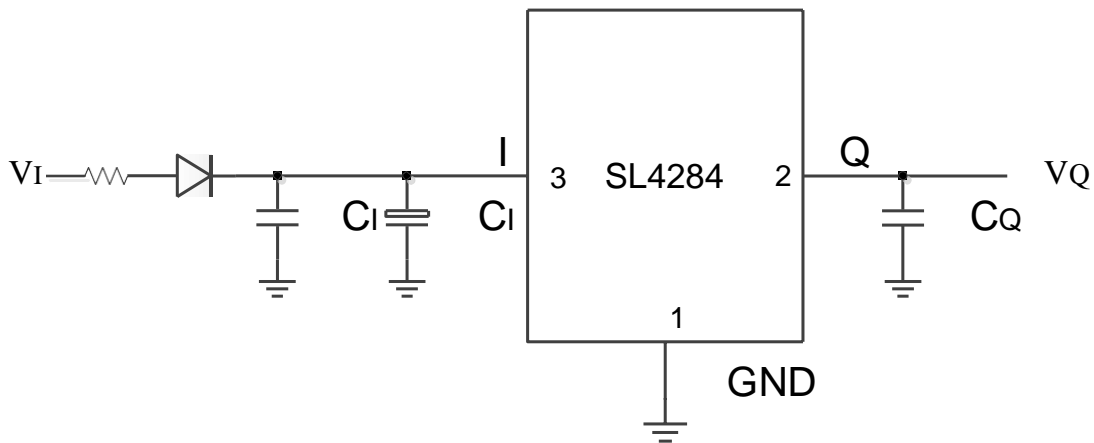
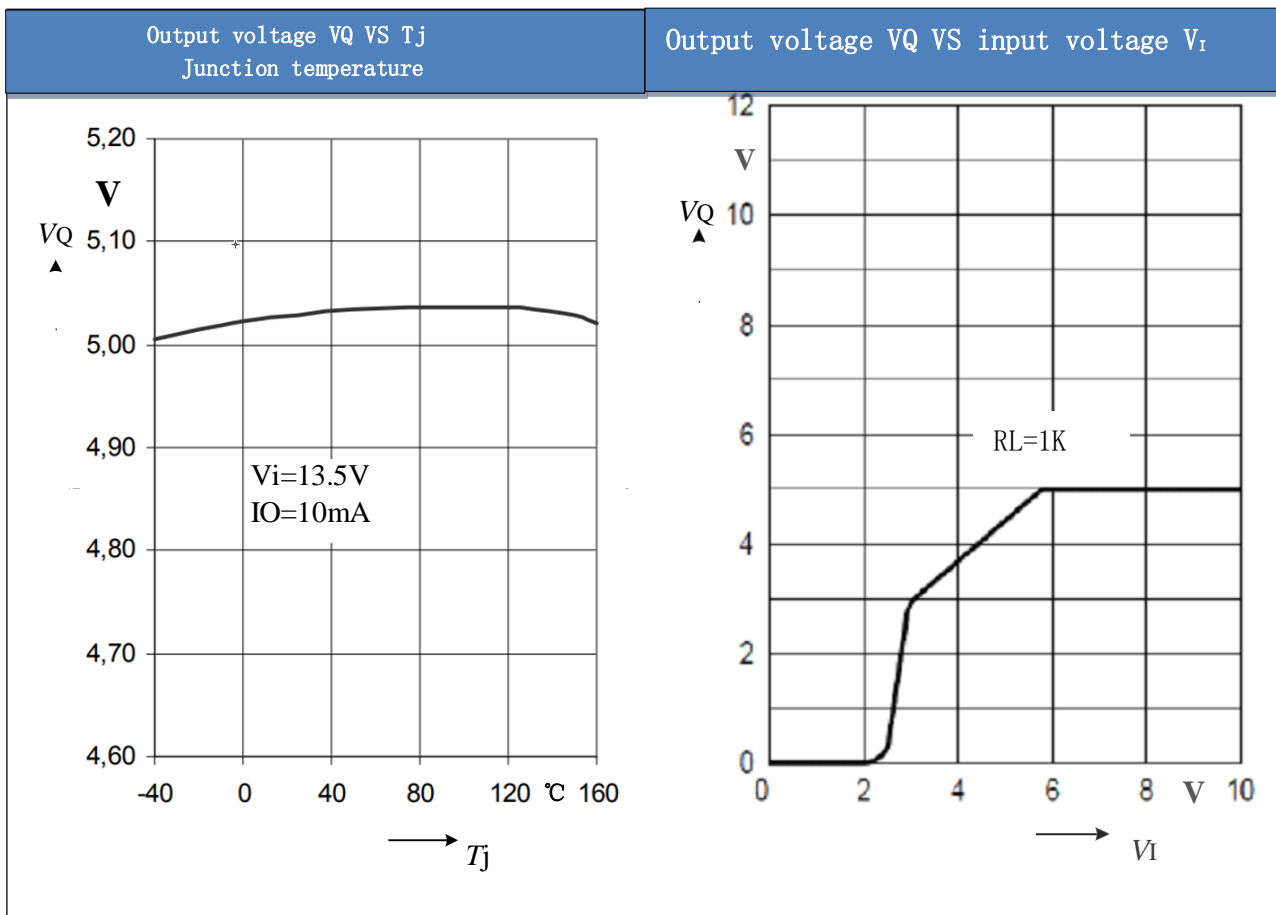
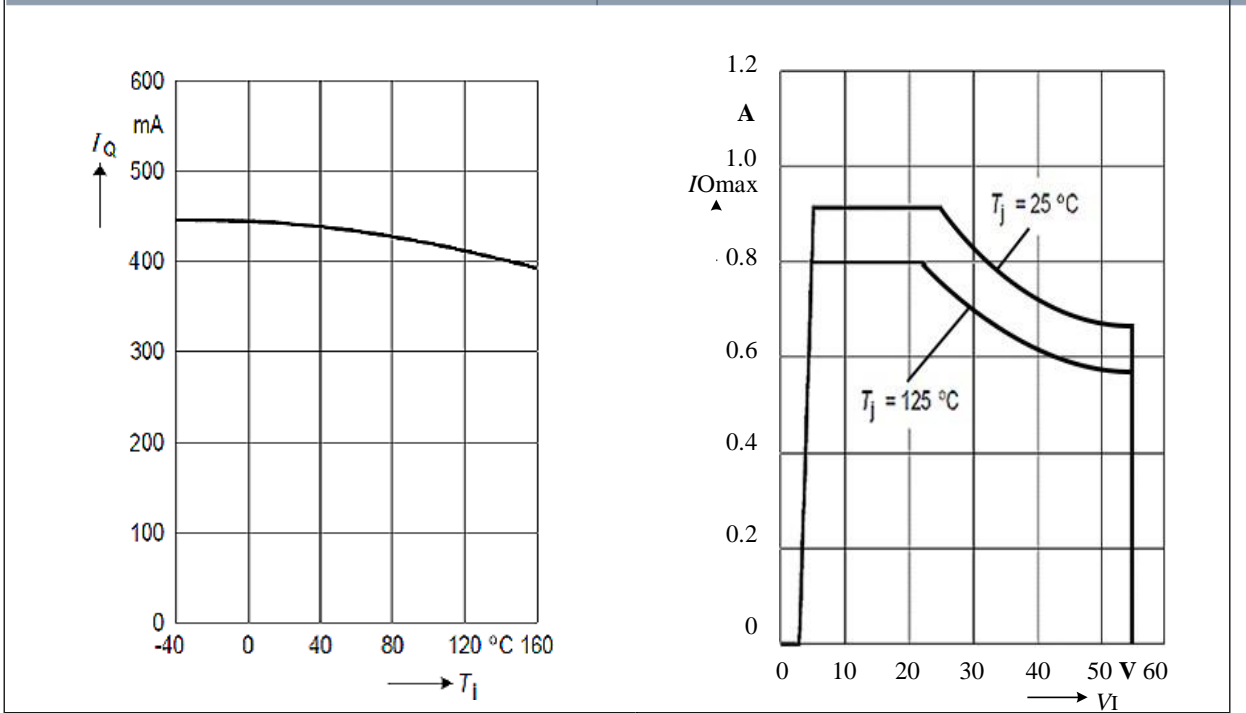


Figure 5-1 Typical application circuit

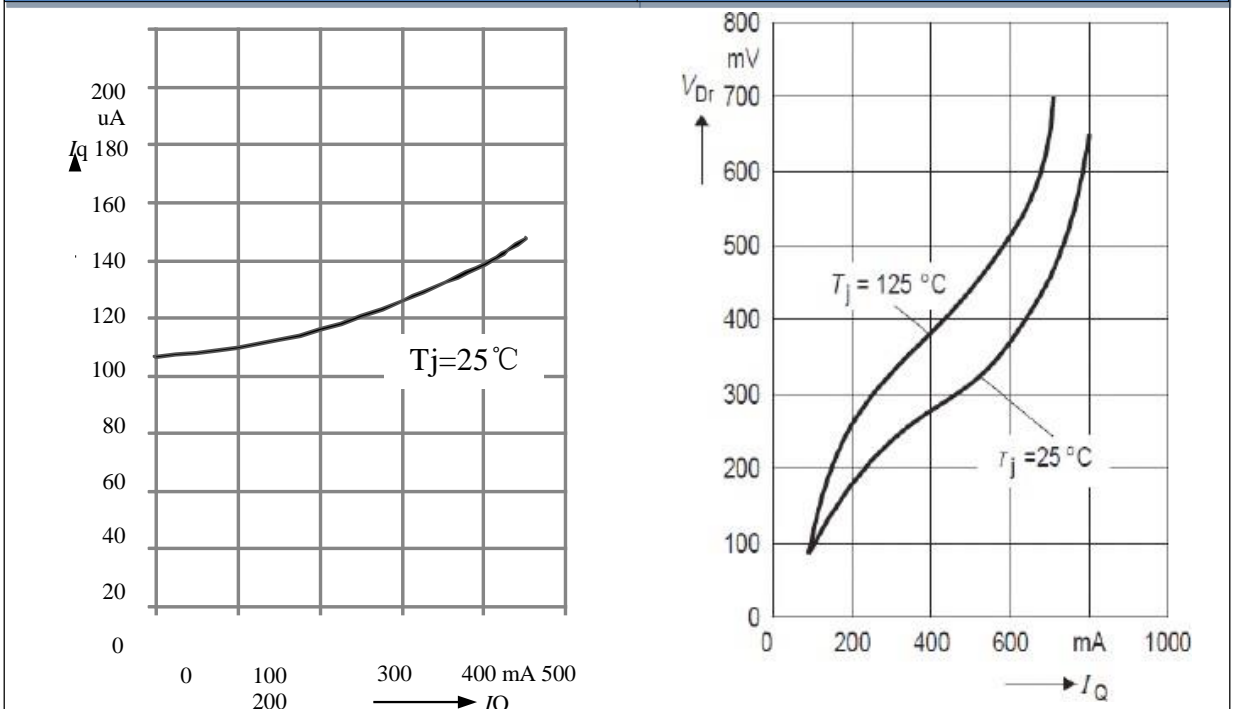
6. Typical characteristic curve



Output voltage V_Q VS T_j Junction temperature	Output Limited Current I_Q VS input voltage V_I
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Quiescent current I_Q VS output current I_O	Voltage difference V_{Dr} VS output current I_O
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7. Package dimension

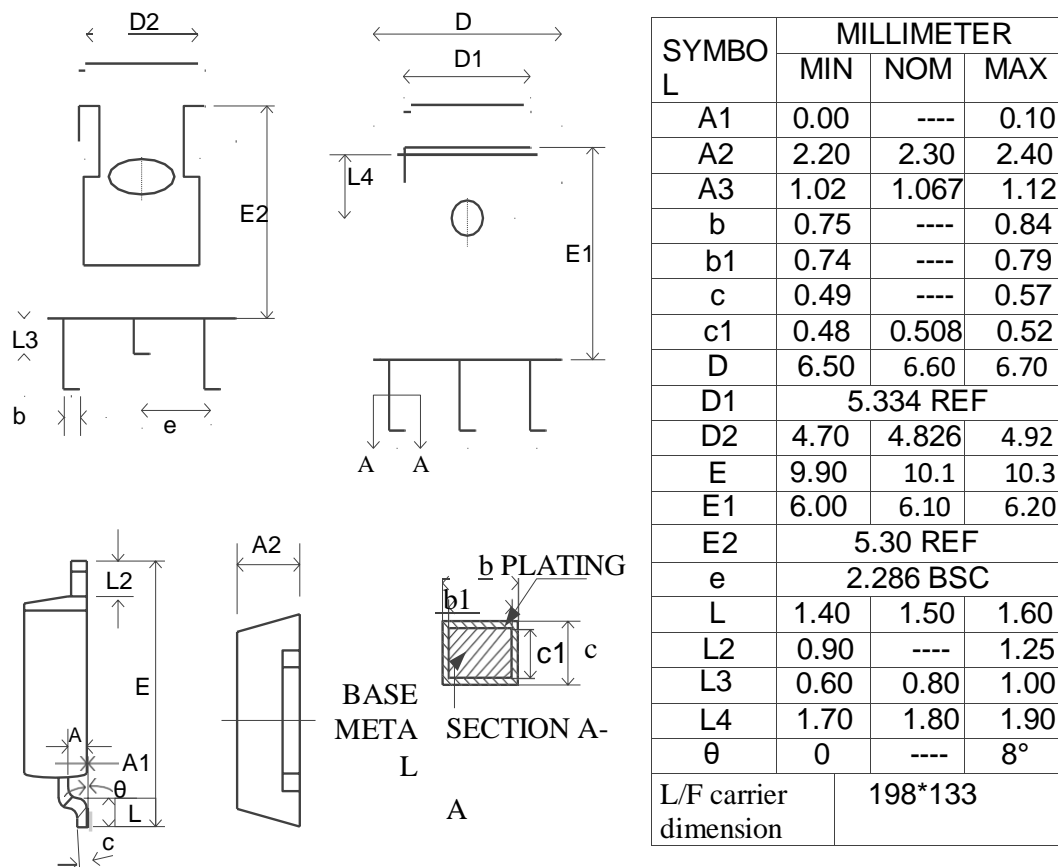


Figure 7-1 Package TO252-3

Green Product (RoHS Compliant)

In order to meet the environmental protection requirements of global customers' products and comply with government regulations, the chip is a RoHS compliant green product (ie, lead-free certification, according to IPC/JEDRC J-STD-020, suitable for lead-free soldering).